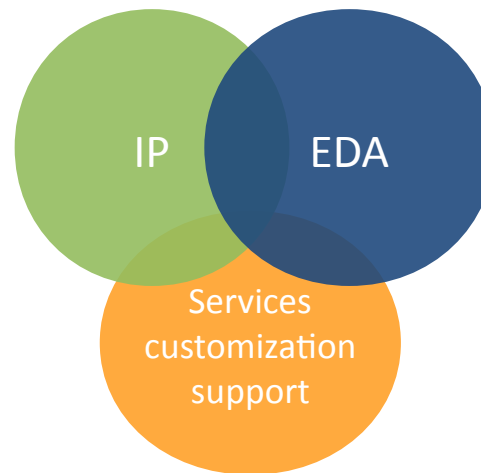


FLEXRAS[®] TECHNOLOGIES

EDA, IP for FPGA solutions



www.flexras.com

contact@flexras.com

FLEXRAS TECHNOLOGIES
Tour Pleyel Ouest
153 Boulevard Anatole France
93200 Saint-Denis, France

FLEXRAS is a provider of innovative EDA tools and IPs for the FPGA and SoC market

- Based in Paris, France.
- Founded in 2009 by three doctors in microelectronics, professor Habib Mehrez and Christian Masson who was Manager of a CAD team at BULL.
- The technology is based on award-winning research led at the LIP6 Lab of University Paris 6.
- The team includes 8 FPGA and EDA engineers.

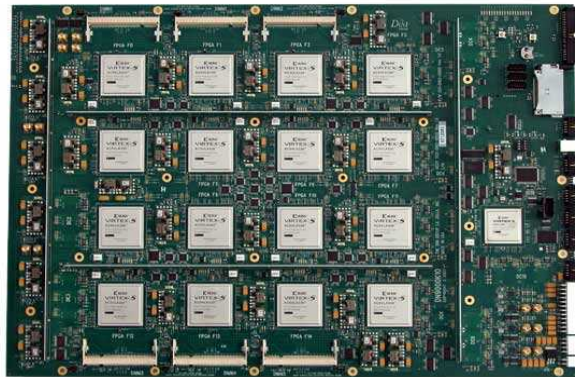
Awards:



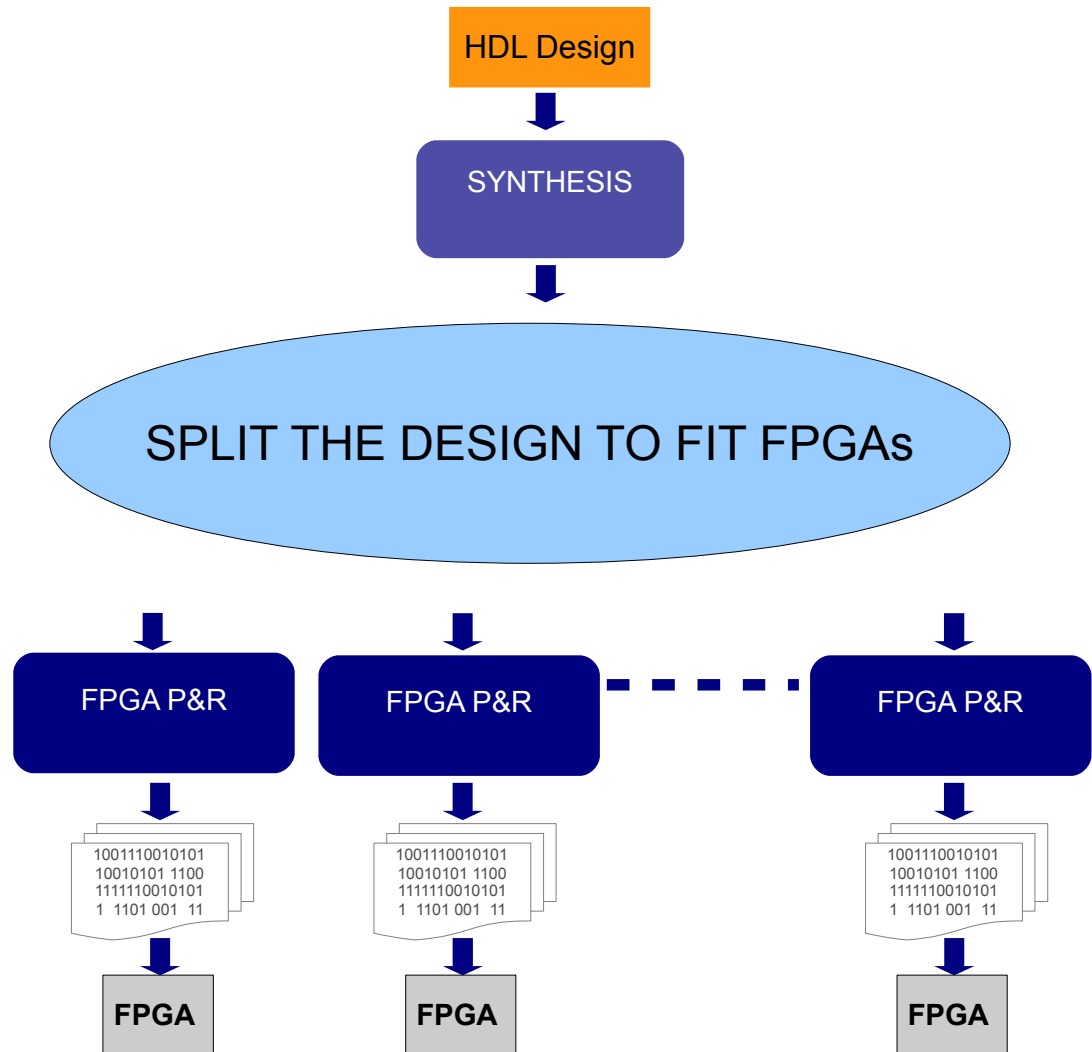
Partners:



Current Rapid Prototyping

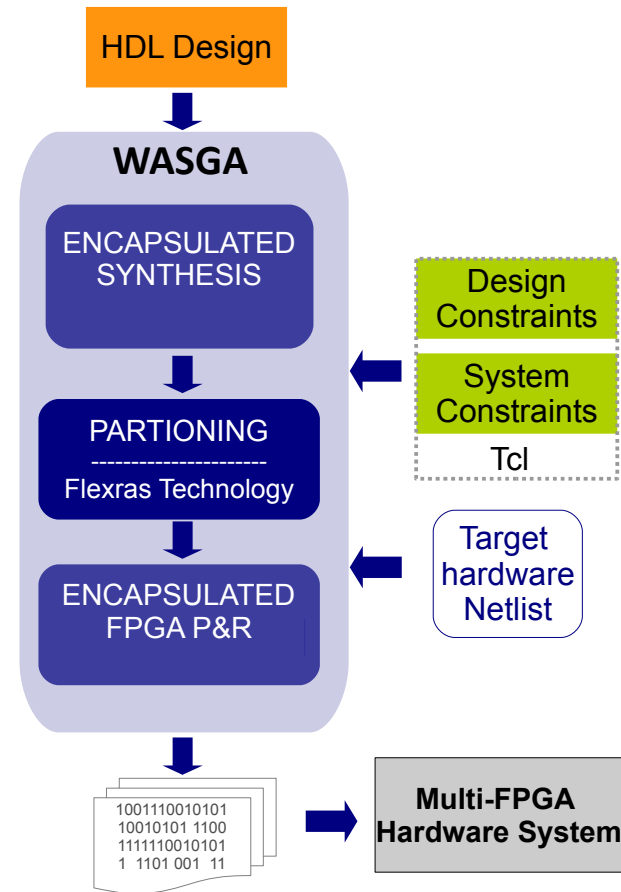


FPGA-based rapid prototypes accelerates design validation prior to silicon availability.



WASGA™ Compiler boosts rapid prototyping

- Proven automatic partitioning of complex designs
- Widely used by IC designers
- 1st time ever that 1 billion gates design is automatically partitioned



WASGA™ partitioning technology is licensed to a leading ASIC emulation provider

Maximizes prototyping system performances

- **5X Better quality of results over existing tools**
 - => Enables multi design iteration per day
 - => Decrease the total cost of IC design

- **Meets performances required for running software**
 - => Validate the software in real time before IC availability
 - => Improve Time-to-Market

Proprietary software technology

Software implementing proprietary techniques and algorithms

Team domain expertise

4 PhD, Former CAD team Manager at BULL, Specialized Engineers and specialized Professor: EDA, FPGA, ASIC

Partnership with LIP6 Lab

FLEXRAS falls within the scope of exploitation of research carried out at the LIP6-UPMC Lab on reconfigurable architectures and EDA topics during the last 10 years.

3 PhD Thesis, ANR-ASTECAS project, FEDER-PPR project

